

PATENT

Applicant(s): En-Hsing Chen et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE WRITE PULSE PROGRAMMING OF INDIVIDUAL MEMORY CELLS AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,844

Filed: December 5, 2003

Examiner: Mai, Son Luu

Group Art Unit: 2818

Atty. Docket No.: 023-0030

September 20, 2004

Mail Stop Amendment  
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- ☒ Form(s) PTO-1449 (1 pages), including copy(ies) of 2 reference(s).  
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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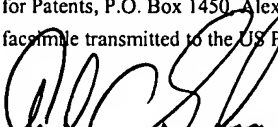
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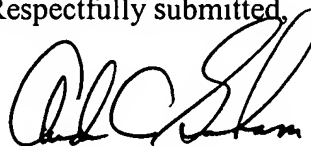
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 Andrew C. Graham	<u>9/20/04</u> Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 023-0030		
				Application No.: 10/729,844		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): En-Hsing Chen et al.		
(Use several sheets if necessary)				Filing Date: December 5, 2003		
				Group Art Unit: 2818		
				Date Submitted: September 20, 2004		
<b>U.S. Patent Documents</b>						
*Examiner Initial		Document Number	Date	Name		
	AA	4,646,266	24 Feb 1987	Ovshinsky et al.		
	AB	5,514,907	07 May 1996	Moshayed		
	AC	6,005,270	21 Dec 1999	Noguchi		
	AD	6,061,270	09 May 2000	Choi		
	AE	6,163,048	19 Dec 2000	Hirose et al.		
	AF	6,420,215	16 Jul 2002	Knall et al.		
	AG	6,456,528	24 Sep 2002	Chen		
	AH	US 2001/0055838 A1	27 Dec 2001	Walker et al.		
	AI					
	AJ					
	AK					
<b>Foreign Patent Documents</b>						
					<b>Translation</b>	
		Document	Date	Country	Yes	No
	AL	0 528 367 A1	24 Feb 1993	Europe		
	AM	0 575 051 A1	22 Dec 1993	Europe		
	AN					
	AP					
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	AR					
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